

REMARKS

Claims 18-20 are pending in the application. Claim 18 is independent. Claims 1-17 are sought to be canceled. These changes are believed to introduce no new matter and their entry is respectfully requested.

Objection to the Specification

In the Office Action, the Examiner objected to the Specification. In the present Response, Applicants seek to amend the Specification to accommodate the objection.

Objection to Claims 2-3, 5-6, 8, and 13

In the Office Action, the Examiner objected to claims 2-3, 5-6, 8, and 13. In the present Response, Applicants seek to cancel claims 2-3, 5-6, 8, and 13 to render the objections moot.

Rejection of Claims 1-17 Under 35 U.S.C. § 102(b)

In the Office Action, the Examiner rejected claims 1-17 under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,612,963 to Koenemann et al. ("Koenemann"). The Examiner as rejected claims 1-5, 10-13, and 15-17 under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,612,963 to Motika et al. ("Motika"). Applicants respectfully traverse the rejections.

A claim is anticipated only if each and every element of the claim is found in a reference. (M.P.E.P. § 2131 *citing Verdegaal Bros. v. Union Oil Co. of California*, 814 F.2d 628 (Fed. Cir. 1987)). The identical invention must be shown in as complete detail as is contained in the claim. *Id. citing Richardson v. Suzuki Motor Co.*, 868 F.2d 1226,1236 (Fed. Cir. 1989)).

In the present Response, Applicants seek to cancel claims 1-17 to render the rejections moot.

Applicants also respectfully submit that Koenemann and/or Motika do not anticipate new claims 18-20. Specifically, Koenemann and/or Motika fail to teach"

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“a weighted random pattern generator *on the die*, the weighted random pattern generator having:

a pseudo-random pattern generator to generate pseudo-random binary bits;

a *memory* to store $m \times p$ multi-bit vectors, the vectors to determine weights to be applied to the pseudo-random binary bits;

set of weighted pseudo-random bit generators corresponding to the set of scan chains;

circuitry to retrieve the weights from memory *and to distribute the weights* to the set of weighted pseudo-random bit generators, the weighted pseudo-random bit generators to apply the weights to the pseudo-random binary bits to generate weighted pseudo-random bits; and

a *controller to control the retrieval and distribution* of the weights” as recited by claim 1.

CONCLUSION

Applicants respectfully submit that all grounds for objection and rejection have been properly accommodated or traversed and that the application is now in condition for allowance. The Examiner is invited to telephone the undersigned representative if the Examiner believes that an interview might be useful for any reason.

Respectfully submitted,

BLAKELY SOKOLOFF TAYLOR & ZAFMAN LLP

Date:

April 14, 2004

Jan Little-Washington
Reg. No. 41,181

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